

***Amendments to the Claims***

This listing of claims will replace all prior versions, and listings of claims in the application.

1. A method of continuously tuning a transconductance comprising:  
coupling a degeneration resistance from a first source of a first transistor in a differential pair of transistors to a second source of a second transistor in the differential pair of transistors:  
applying a second variable degeneration resistance in parallel to the first degeneration resistance in response to the application of a first variable control voltage; and  
applying a third variable degeneration resistance in parallel to the first degeneration resistance and the second degeneration resistance in response to the application of a second variable control voltage having a fixed voltage offset from the first variable control voltage.

Claims 2-18 are cancelled.